

10/582145

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Group Art Unit Unassigned

In re Patent Application of

WADA, Shigeki

Application No. Unassigned

International Application No:
PCT/JP2004/017573

International Filing Date: 26 November 2004

Confirmation No.: Unassigned

Filed: Herewith

Examiner: Unassigned

"CLOCK AND DATA RECOVERY
CIRCUIT"

Atty Reference: 205024-9003

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR §1.97(b)

Mail Stop: PCT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The Examiner's attention is directed to the references which are listed on the attached Forms PTO/SB/08A and PTO/SB/08B. Copies of the non-U.S. patent references are also attached.

Citation of these references is respectfully requested.

No concession is made that these documents are prior art, and Applicant expressly reserves the right to antedate the documents as may be appropriate.

Date: June 7, 2006

Respectfully submitted,



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312.222.0800

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Substitute of form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/582145
		Filing Date	
		First Name Inventor	WADA, Shigeki
		Group Art Unit	
		Examiner Name	
Sheet 1 of 1	Attorney Docket Number	205024-9003	

U.S. PATENT DOCUMENTS				
Examiner Initials		U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document
		2004/0207437	Shibahara et al.	10/21/2004
		6,249,160	Tagami et al.	6/19/2001

FOREIGN PATENT DOCUMENTS							
Examiner Initials		Country Code	Foreign Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Translation	English Abstract
		JP	5-327683	FUJITSU LTD.	12/10/1993		
		JP	9-148922	FUJITSU LTD.	6/6/1997		
		JP	2000-216763	NIPPON ELECTRIC CO.	8/4/2000		X
		JP	2000-228660	MITSUBISHI ELECTRIC CORP.	8/15/2000		X
		JP	2001-127626	YAMAHA CORP.	5/11/2001		
		JP	2001-156630	MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.	6/8/2001		
		JP	2001-345793	MIYAGI NIPPON DENKI KABUSHIKI KAISHA	12/14/2001		
		JP	2004-235842	KABUSHIKI KAISHA RUNESASU TECHNOLOGY	8/19/2004		

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				<i>Complete if Known</i>	
				Application Number	1U/582145
				Filing Date	
				First Named Inventor	WADA, Shigeki
				Group Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	205024-9003

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS		
Examiner Initials		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, pages(s), volume-issue numbers(s), publisher, city and/or country where published.
		RAZAVI, Behzad; "DESIGN OF INTEGRATED CIRCUITS FOR OPTICAL COMMUNICATIONS"; pp. 296-297; published by McGraw-Hill Higher Education; New York, NY.
		International Search Report, International Patent Application No. PCT/JP2004/017573, mailed March 8, 2005.

Examiner Signature		Date Considered	
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